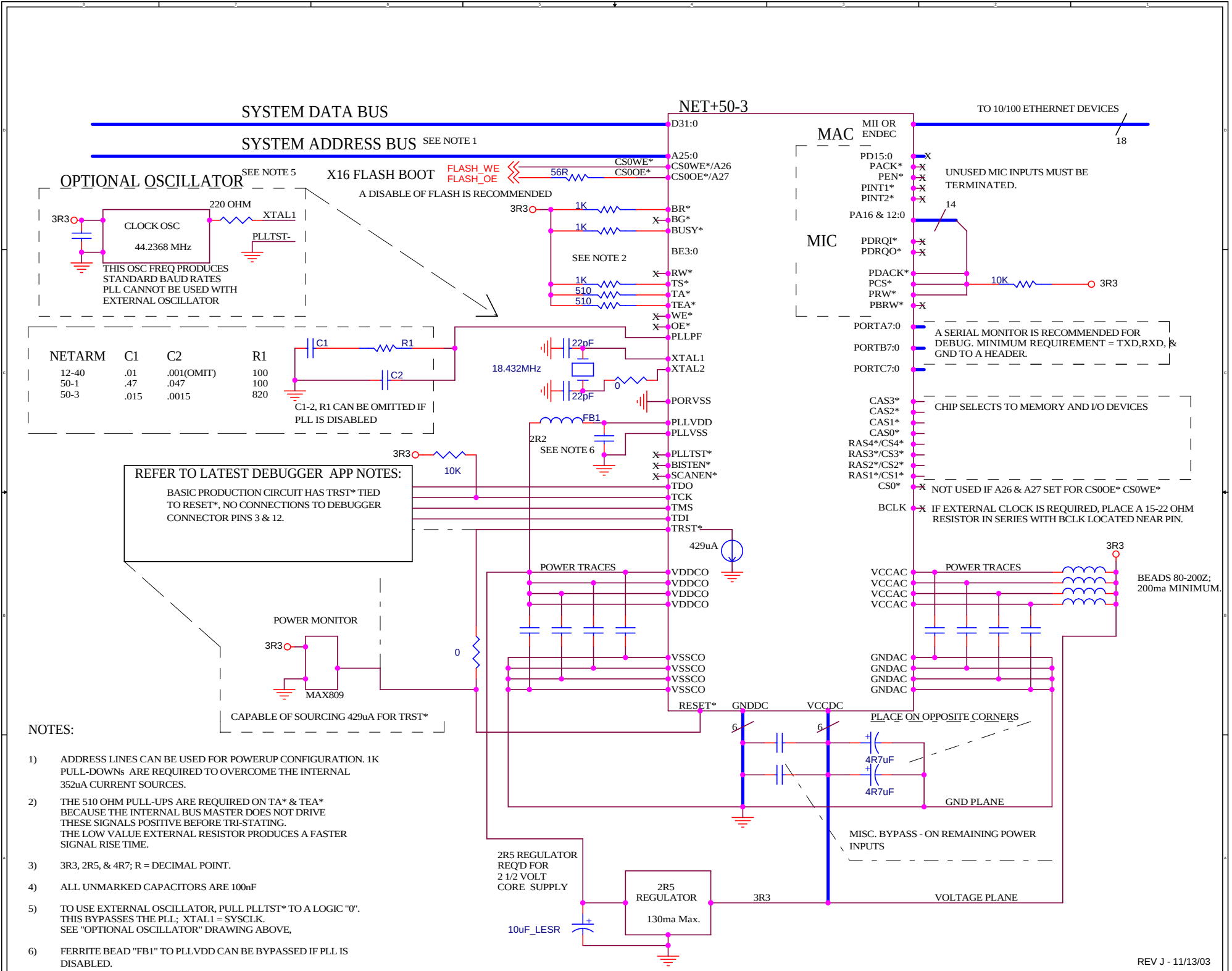

NOTES:

1) ADDRESS LINES CAN BE USED FOR POWERUP CONFIGURATION. 1K PULL-DOWNs ARE REQUIRED TO OVERCOME THE INTERNAL 352uA CURRENT SOURCES.

2) THE 510 OHM PULL-UPS ARE REQUIRED ON TA* & TEA* BECAUSE THE INTERNAL BUS MASTER DOES NOT DRIVE THESE SIGNALS POSITIVE BEFORE TRI-STATING. THE LOW VALUE EXTERNAL RESISTOR PRODUCES A FASTER SIGNAL RISE TIME.

3) 3R3, 2R5, & 4R7; R = DECIMAL POINT.

4) ALL UNMARKED CAPACITORS ARE 100nF

5) TO USE EXTERNAL OSCILLATOR, PULL PLLTST* TO A LOGIC "0". THIS BYPASSES THE PLL; XTAL1 = SYSCLK. SEE "OPTIONAL OSCILLATOR" DRAWING ABOVE,

6) FERRITE BEAD "FB1" TO PLLVDD CAN BE BYPASSED IF PLL IS DISABLED.