



NS9xxx-0 Demo Board Jumpers Guide



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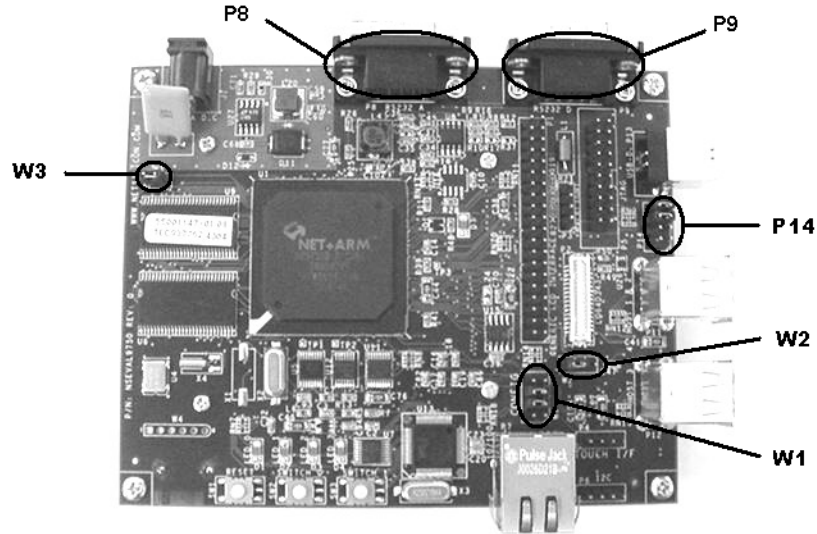
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NS9xxx-0 Demo Board Jumpers

This document describes the jumpers on the NS9xxx-0 demo board.

NS9xxx-0 demo board jumper and pin list

This image shows the NS9xxx-0 demo board. The pin jumper blocks are labeled W1-W3 and P8, P9, and P14:



Be aware that the markings on the board for the 9-pin DB connectors are reversed:

- The connector marked **RS232A** is port D (**P8**).
- The connector marked **RS232D** is port A (**P9**).

This table describes the jumpers:

Pin jumper block description	Pin	Pin description
W1: 4x2-pin jumper block	1 + 2	<ul style="list-style-type: none"> ■ Production mode. ■ No jumper: debug mode (default). For the JTAG to operate, the board must be in debug mode.
	3 + 4	<ul style="list-style-type: none"> ■ PLL enable. ■ No jumper disables the PLL (default).
	5 + 6	<ul style="list-style-type: none"> ■ Big endian (default.) ■ No jumper: little endian.
	7 + 8	<ul style="list-style-type: none"> ■ Boot from SPI boot. ■ No jumper: boot from flash ROM (default).
W2: 3-pin jumper block	1 + 2	3.3 volts for the LCD (default).
	2 + 3	5 volts for the LCD.
W3: 2-pin jumper block	1 + 2	<ul style="list-style-type: none"> ■ Flash enable (default). ■ No jumper: flash disable. <p>If the board is booting from flash ROM (W1, no jumper on pins 7 + 8), and the code in flash becomes corrupted, it may prevent the in-circuit emulator (ICE), or debugger, from starting.</p> <p>In such a case, disable the flash code by removing this jumper. When the ICE starts running, add the jumper back to enable the flash ROM to be reprogrammed.</p>
P14: 4x2-pin jumper block	1 + 2 and 3 + 4	USB device mode.
	5 + 6 and 7 + 8	USB host mode (default).



PN:(1P)90000685 A